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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

DARREN LEE ANAND, ET AL.

SERIAL NO.: 10/604,414

FILED: JULY 18, 2003

FOR: METHOD FOR REDUCED
ELECTRICAL FUSING TIME

ATTY. DOCKET NO.: BUR920020093US1

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EXAMINER: MARY C. BARAN

ART UNIT: 2857

RESPONSE EMAIL STOP AF
COMMISSIONER FOR PATENTS
PO BOX 1450
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SIR:

Applicants submit this response to the rejection of the claims in the present Application in light of additional evidence provided herewith. An RCE was filed in this Application on January 10, 2006. No extension of time is required and no fee is required to submit this Request. However, should any other fee be required to further the prosecution of this application, please charge that fee to IBM CORPORATION'S Deposit Account No. 09-0456.

CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(A)]

I hereby certify that this correspondence is being:

☐ deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.☒ transmitted by facsimile on the date shown below to the U.S. Patent and Trademark Office at (571) 273-8300.

2-22-06

Date

Shenise Ramdeen
Shenise Ramdeen